

## EAST Search History

## EAST Search History (Prior Art)

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L9	104653	(multiplexer)	USPAT	OR	ON	2009/11/12 22:40
L10	8682	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) )	USPAT	OR	ON	2009/11/12 22:40
L11	1225	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) )	USPAT	OR	ON	2009/11/12 22:40

L12	622	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3)	USPAT	OR	ON	2009/11/12 22:40
L13	272	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ((direct) adj (memory) adj (access)) or (DMA) )	USPAT	OR	ON	2009/11/12 22:40

L14	94	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) ) )	USPAT	OR	ON	2009/11/12 22:40
L15	16	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) ) AND ( ((input) or (source) or (channel)) adj	USPAT	OR	ON	2009/11/12 22:40

		((identifier) or (identification) or (ID)) )				
L16	27	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) ) AND ( ((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)) )	USPAT	OR	ON	2009/11/12 22:40
L17	13	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND	USPAT	OR	ON	2009/11/12 22:40

		( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) ) AND ( ((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)) ) AND ( (output) adj ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) )				
L18	16	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) ) AND ( ((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)) ) AND ( (output) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) )	USPAT	OR	ON	2009/11/12 22:40

L19	27	(multiplexer\$1) AND ( ((multiple) or (plural \$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (plural\$5) or (several)) adj ((output \$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) ) AND ( ((input) or (source) or (channel)) adj2 ((identifier) or (identification) or (ID)) )	USPAT	OR	ON	2009/11/12 22:40
L20	119	(multiplexer\$1) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj	USPAT	OR	ON	2009/11/12 22:40

		(memory) adj (access)) or (DMA ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC) )				
L21	52	(multiplexer\$1) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC) ) AND ( ((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)) )	USPAT	OR	ON	2009/11/12 22:40
L22	51	(multiplexer\$1) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1)) ) AND	USPAT	OR	ON	2009/11/12 22:40

		( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC) ) AND ( ((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)) )AND ( ((output) or (transmit\$4)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) )				
L23	51	(multiplexer\$1) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC) ) AND ( ((input) or	USPAT	OR	ON	2009/11/12 22:40



		(source) or (channel)) near2 ((identifier) or (identification) or (ID)) )AND ( ((output) or (transmit\$4) or (transmission)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) )				
L24	51	(multiplexer\$1) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((input\$1) or (channel \$1)) ) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((multiple) or (multi) or (plural\$5) or (several)) adj ((output\$1) or (channel\$1)) ) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) AND (priorit\$3) AND ( ( ((direct) adj (memory) adj (access)) or (DMA) ) near1 ((controller\$1) or (manager\$1) or (unit\$1)) or (DMAC) ) AND ( ((input) or (source) or (channel)) near2 ((identifier) or (identification) or (ID)) )AND ( ((output) or (transmit\$4) or (transmission)) near2 ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) )	USPAT	OR	ON	2009/11/12 22:40

L25	2214	((370/535) or (370/536) or (370/537) or (370/539) or (370/540) or (370/541) or (370/542) or (370/544)).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L26	1045	(370/535).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L27	298	(370/536).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L28	712	(370/537).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L29	153	(370/539).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L30	120	(370/540).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L31	81	(370/541).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L32	167	(370/542).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L33	44	(370/544).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L34	9194	(multiplexer\$1) AND ((input\$1) or (channel \$1)) AND ((memor \$4) or (FIFO\$1) or (buffer\$1) or (queue \$1) or (register\$1)) AND ( ((least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) )	USPAT	OR	ON	2009/11/12 22:40

L35	5503	( (multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) ) AND ( ( (least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) )	USPAT	OR	ON	2009/11/12 22:40
L36	242	( (multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) SAME (priorit \$3) ) AND ( ( (least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) )	USPAT	OR	ON	2009/11/12 22:40
L37	61	( (multiplexer\$1) SAME ((input\$1) or (channel\$1)) SAME ((memor\$4) or (FIFO \$1) or (buffer\$1) or (queue\$1) or (register \$1)) SAME (priorit \$3) ) AND ( ( (least) adj (significant) adj (bit)) or (LSB) ) AND ( ((most) adj (significant) adj (bit)) or (MSB) ) ) AND ( ((direct) adj (memory) adj (access)) or (DMA) )	USPAT	OR	ON	2009/11/12 22:40

L38	6207	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) AND ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) AND ( ((source) adj (identification\$1)) or (identification\$1) or ("ID") ) AND (priority)	USPAT	OR	ON	2009/11/12 22:40
L39	21	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or (identification\$1) or ("ID") ) SAME (priority)	USPAT	OR	ON	2009/11/12 22:40
L40	623	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or (identification\$1) or ("ID") or (source\$1) ) SAME ((priority) or (order\$2))	USPAT	OR	ON	2009/11/12 22:40

L41	238	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or (identification\$1) or ("ID") or (source\$1) ) SAME ((priority) or (order\$2)) SAME (output\$1)	USPAT	OR	ON	2009/11/12 22:40
L42	712	(370/537).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L43	5	L40 and L42	USPAT	OR	ON	2009/11/12 22:40
L44	3	L41 and L42	USPAT	OR	ON	2009/11/12 22:40
L45	1	("7016337").PN.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L46	71004	(370/\$5).CCLS.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L47	118	L40 and L46	USPAT	OR	ON	2009/11/12 22:40
L48	62	L41 and L46	USPAT	OR	ON	2009/11/12 22:40
L49	49	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or (identification\$1) or ("ID") ) SAME ((priority) or (order \$2)) SAME (output \$1)	USPAT	OR	ON	2009/11/12 22:40

L50	372	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority) or (order\$2)) SAME (output\$1)	USPAT	OR	ON	2009/11/12 22:40
L51	492	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or	USPAT	OR	ON	2009/11/12 22:40

		(identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority) or (order\$2))				
L52	493	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority) or (order\$2) or ((quality) adj2 (service\$1)) or (QOS))	USPAT	OR	ON	2009/11/12 22:40
L53	392	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj	USPAT	OR	ON	2009/11/12 22:40

		(address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3))				
L54	393	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent))	USPAT	OR	ON	2009/11/12 22:40
L55	88	L54 and L46	USPAT	OR	ON	2009/11/12 22:40



L56	8	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ( ((pre) adj (determin\$3)) or (predetermin\$3) or (known) ) near2 ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) ) SAME ((priority) or (order \$2)) SAME ((output \$1) or (transmit) or (transmitt\$3) or (sent))	USPAT	OR	ON	2009/11/12 22:40
L57	10	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1)) SAME ( ( ((pre) adj (determin\$3)) or (predetermin\$3) or (known) ) near2 ( ((source) adj (identification\$1)) or	USPAT	OR	ON	2009/11/12 22:40

		((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) ) SAME ((priority) or (order \$2))				
L58	4	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1) or (register \$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent)) SAME ( ((direct) adj (memory) adj (access)) or (DMA) )	USPAT	OR	ON	2009/11/12 22:40

L59	409	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1) or (register \$1)) SAME ( ((source) adj (identification\$1)) or ((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent))	USPAT	OR	ON	2009/11/12 22:40
L60	40	L59 AND ( ((direct) adj (memory) adj (access)) or (DMA) )	USPAT	OR	ON	2009/11/12 22:40
L61	1	("6898182").PN.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L62	409	( ((multi) or (multiple) or (plural) or (plurality)) adj ((input\$1) or (source \$1)) ) SAME ((memory) or (memories) or (storage) or (queue \$1) or (buffer\$1) or (table\$1) or (register \$1)) SAME ( ((source) adj (identification\$1)) or	USPAT	OR	ON	2009/11/12 22:40

		((source) adj (identifier\$1)) or ((source) adj ("ID")) or ((source) adj (address\$2)) or ((port) adj (identification\$1)) or ((port) adj (identifier \$1)) or ((port) adj ("ID")) or ((port) adj (address\$2)) or (identification\$1) or (identifier\$1) or ("ID") or (address \$2) ) SAME ((priority) or (order\$2)) SAME ((output\$1) or (transmit) or (transmitt\$3) or (sent))				
L63	40	L62 AND ( ((direct) adj (memory) adj (access)) or (DMA) )	USPAT	OR	ON	2009/11/12 22:40
L64	1	("7310315").PN.	USPAT; USOCR	OR	OFF	2009/11/12 22:40
L65	1	("7373421").PN.	USPAT; USOCR	OR	OFF	2009/11/12 22:40

### EAST Search History (I nterference)

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**11/ 12/ 2009 10:49:28 PM**

**C:\ Documents and Settings\ mmais\ My Documents\ EAST\ Workspaces\ 10,690,324 [10-7-2009].wsp**